VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY CENTER FOR VLSI AND NANOTECHNOLOGY

Recruitment for the Chips to System Design -Special Manpower development Program (C2SD-SMDP) sponsored by Ministry of Electronics and Information Technology (MEITY).

1. SOC Engineer

| Qualificatio | n | | | | | | 2 | | | Salary | |
|---|-----------|---------|-------|----------|-----------|---------------|--------------|--------|--------|---------|---------------|
| Candidate | should | hold | а | Ph.D | and/or | M.Tech. | Degree | in | VLSI | The | remuneration |
| Design/Micr | oelectron | ics/Nan | oeled | ctronics | from a | recognized | university. | Ha | ving a | will be | Rs.30,000 per |
| working knowledge of Linux, VLSI software tools is advantageous. Candidates with | | | | | | | | month. | | | |
| experience of VLSI Chip Design, fabrication and test, Embedded Systems, Linux and | | | | | | | | ux and | | | |
| Programmin | g languag | e and F | PGA | implem | nentation | related proje | ects will be | prefe | erred. | | |

2. Project Associate

| Qualification | Salary | | |
|--|--------------------|--|--|
| Essential: B. Tech in Electronics / Electronics and Comm. or equivalent with first | The remuneration | | |
| class or Equivalent. Candidates with experience of VLSI Chip Design and test, | will be Rs. 20,000 | | |
| Embedded Systems, Linux and Programming language and FPGA implementation | per month. | | |
| related projects will be preferred. | | | |

3. Research Associate Qualification Salary Essential: B. Tech in Electronics /Computer Science & Engg./ Electrical Engineering or M.Sc. in Electronics/Physics or Equivalent with first class or Equivalent. The remuneration will be Rs. 20,000 will be Rs. 20,000 per month. Candidates with experience in measurements and test, Embedded System, Linux and Programming language and FPGA implementation related projects will be preferred. per month.

Candidates having qualifying GATE score will be preferred for all these posts. The duration of the post will be initially for one year and may be extended further subject to satisfactory performance and provisions under the Project. The posts will be coterminous with the duration of the project. Interested candidates may apply on or before 1st July 2019 with a copy of resume mentioning Educational Qualifications Professional Experience (if any), contact details including email and phone numbers. Please mention the "C2SD SMDP Project" on top of the envelop and mail it to "Director, VNIT, South Ambazari Road Nagpur 440010. Final year students who have already given examination may also apply. However, they will have to submit the mark list, indicating that have acquired qualifications as per mentioned in this advertisement, on or before 30th Sept 2019.

Note: Mere fulfillment of qualifications/experience requirements does not entitle a candidate to be called for the interview. Candidates will be shortlisted based on their academic performance/professional experience and only the shortlisted candidates will be called for an interview. Selected candidate will be intimated by email. No correspondence whatsoever will be entertained from candidates regarding reasons for not being called for interview, conduct and result of interview. No TA/DA is permissible for attending the interview. The Institute reserves the right to: (a) conduct written/trade tests for such posts wherever if the circumstances so warrant (b) not filling any of the advertised positions (c) fill consequential vacancies arising at the time of interview from available candidates. The number of positions is thus open to change. **Director**