

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY

CENTER FOR VLSI AND NANOTECHNOLOGY

Recruitment for the Chips to System Design -Special Manpower development Program (C2SD-SMDP) sponsored by Ministry of Electronics & Information Technology (MEITY).

1. Lab Engineer -1 No.

Qualification	Salary
Candidate should hold M. Tech in Electronics/VLSI from a recognized university with experience in research work in the VLSI area. Candidate with experience in measurement of vibration will be preferred. Having working knowledge on windows and Linux, VLSI software tools and successfully crediting courses related to VLSI is advantageous. Candidates doing Ph.D and submitted the thesis can also apply.	The remuneration will be Rs. 45000 per month.

2. Project Associate - 1 No

Qualification	Salary
Essential: Essential: B. Tech in Electronics /Computer Science & Engg./ Electrical Engineering or M.Sc. in Electronics/Physics or Equivalent with first class or Equivalent. Candidates with experience in VLSI/ testing electronic circuits/ Embedded System, Linux will be preferred.	The remuneration will be Rs.25000 per month.

The duration of the post will be initially for six months and may be extended further subject to satisfactory performance and provisions under the Project. The posts will be coterminous with the duration of the project which is up to 30 Nov 2021. Interested candidates are requested to submit a detailed CV (highlighting the exposure they have in the desired areas) and relevant certificates/evidences by post and/or email with all the contact details including email and phone numbers to the project coordinator as per the details given below by March 22, 2021.

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Center For VLSI and Nanotechnology
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Note: Mere fulfillment of qualifications/experience requirements does not entitle a candidate to be called for the interview. Candidates will be shortlisted based on their academic performance/professional experience and only the shortlisted candidates will be called for an interview. Selected candidate will be intimated by email. No correspondence whatsoever will be entertained from candidates regarding reasons for not being called for interview, conduct and result of interview. No TA/DA is permissible for attending the interview. The Institute reserves the right to: (a) conduct written/trade tests for such posts wherever if the circumstances so warrant (b) not filling any of the advertised positions (c) fill consequential vacancies arising at the time of interview from available candidates. The number of positions is thus open to change.

Director