

DEPARTMENT OF ELECTRICALENGINEERING

VISVESVARAYA NATIONAL INSTITUTE OFTECHNOLOGY

South Ambazari Road, Nagpur – 440010 Maharashtra

Advertisement Date: 03/08/2021

Call for the position of Two Junior Research Fellows for the DST India-European Union (EU) Consortium Research Project

"Sustainable Energy system for Achieving Novel Carbon Neutral Energy Communities (SUSTENANCE)"

Applications are invited from self motivated and eligible candidates for the position of Junior Research Fellow (JRF) for the Department of Science and Technology (DST) India – European Union (EU) Consortium Project on "Sustainable Energy system for Achieving Novel Carbon Neutral Energy communities (SUSTENANCE)" with primary focus on "Development of Smart Solid State Transformer for EV Charging Infrastructure".

Essential Qualifications:

- 1. M.Tech/M.E.in Electrical Engineering Department with specialization in Power Electronics/Electrical Drives/Power System/Control System or equivalent.
- 2. Candidate should have qualified GATE in the past.

Desirable Qualifications:

- 1. Age: Preferably below 30 years.
- 2. Candidate should have experimental knowledge in the **hardware and controller design of power electronic converters**. First division in UG and PG will be given preference.
- 3. Proficient in MATLAB, PSIM, PCB Design Software and Microcontroller/Digital Signal Controller/FPGA.
- 4. Good analytical ability and competent in technical writing skills.

Fellowship:

First two years Rs. 31,000 p.m. (Consolidated) and Third year onwards Rs. 35,000 (consolidated). HRA will be given as per rules.

Duration:

Maximum duration of the project is 42 Months. Initially, appointment will be made for six months, which can be extended later for every six months till the completion of project subject to approval of Director, VNIT. The extension shall be solely based on satisfactory performance.

How to apply:

Interested candidates are requested to submit duly filled and signed application form in the format (available online on the institute website **www.vnit.ac.in**) and detailed Curriculum Vitae through **email** and **hard copy** to the Principal Investigator before the deadlines. Along with the application, candidates have to send: (i) Two Letters of Recommendation, and (ii) One page write up on the skills and achievements.

Note:

- 1. Selection procedure consists of Written Test ad Interview both in *online mode*.
- 2. Candidates selected for this position can also have opportunity to register for regular Ph. D. program provided they satisfy the eligibility criteria for the same.
- 3. Decision of The Director, VNIT Nagpur will be the final in all respect.

Correspondence address (Principal Investigator):

Dr. Pradyumn Chaturvedi,

Assistant Professor, Department of Electrical Engineering,

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Submission deadline for Application Form (e-mail & Hard Copy)24 Aug, 2021Intimation to Shortlisted Candidates (via e-mail)25 Aug, 2021Written Test28 Aug, 2021Interview30 Aug, 2021

DIRECTOR (VNIT, NAGPUR)